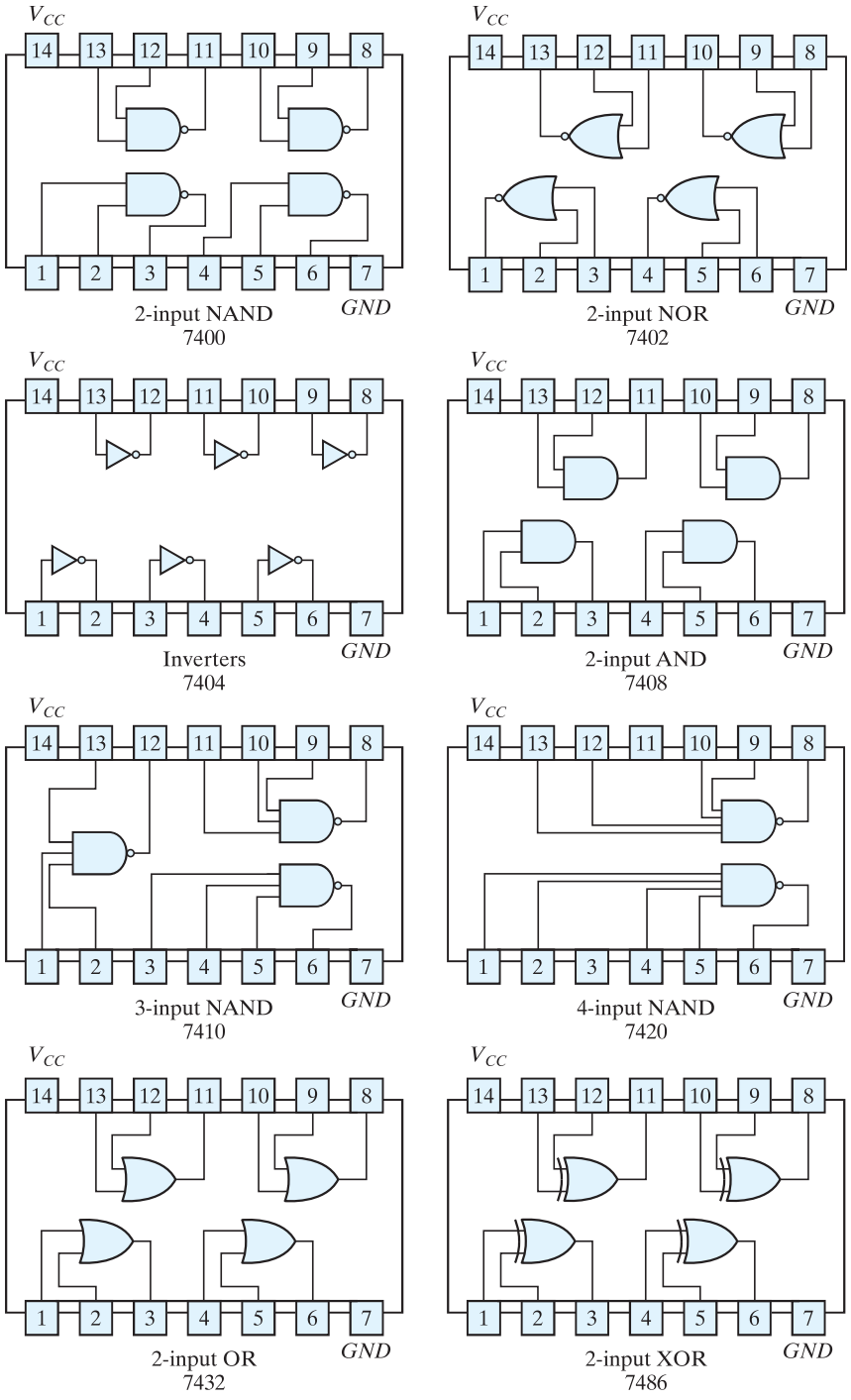

FIGURE 9.1
Digital gates in IC packages with identification numbers and pin assignments